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PATENT NUMBER

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			SCANNED THE	O.I.P.E.	FT	PATENT DATE			
A	PPLICATION NO. 09/546981	CONT/PRIOR	CLASS 709	SUBCLASS 238	ART UI 2-7-51		EXAMINER PRIETO, B.		
APPLICANTS	Anthony Ga Natarajan Gail Woodl	Vaidhvana	athan		<u></u>	<u> </u>	-		
TITLE	Method for processor	bridging	; control	point dat	a fra	mes u	sing a network		

	ISSUING CLASSIFICATION																
ORIGINAL								CROSS REFERENCE(S)									
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TERMINAL		DRAWINGS		CLAIMS ALLOWED			
□ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
The term of this patent subsequent to(date)	<u> </u>		·	NOTICE OF ALL	OWANCE MAILED		
has been disclaimed.	(Assistant	Examiner)	(Date)				
The term of this patent shall not extend beyond the expiration date							
of U.S Patent. No.			*.	ISSUE FEE			
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WARNING: The information disclosed herein may be rest Possession outside the U.S. Patent & Tradem	tricted. Unauthorized	disclosure may be p	prohibited by the U	nited States Code Title 35	Sections 122, 181 and 366		